This listing of claims will replace all prior versions, and listings of claims in the application:

**LISTING OF CLAIMS:**

1.    (Currently Amended)  A decoder ~~interface~~ comprising:

an input ~~circuit~~ <u>device</u> that has a port for receiving encoded data <u>embedded in a bit stream with different standards</u> from an external source;

control ~~circuitry~~ <u>device</u> that is coupled to and controls the input ~~circuit~~ <u>device</u> to operate selectively in a first mode to receive ~~raw byte~~ data at the port from said external source, and in a second mode to receive tokens at the port from said external source; and

a ~~plurality of stages, including an initial and an intermediate stage, said stages~~ <u>stage</u> having an associated parser, said tokens having information used by said parser to prepare said ~~stages~~ <u>stage</u> for processing <u>different portions of said bit stream corresponding to said different standards</u>.

2.    (Currently Amended) The decoder ~~interface~~ of claim 1, wherein the port comprises a coded data port.

3.    (Currently Amended) The decoder ~~interface~~ of claim 1, wherein the port comprises a microprocessor ~~interface~~.

4.    (Currently Amended) The decoder ~~interface~~ of claim 2, wherein the port further includes a microprocessor ~~interface~~.

5.    (Currently Amended) The decoder ~~interface~~ of claim 1, wherein the control ~~circuitry~~ <u>device</u> includes a byte mode signal for selecting the first mode or the second mode.

6.    Canceled

7.    (Currently Amended) The decoder ~~interface~~ of claim 1, wherein the received ~~raw byte~~ data is placed into tokens.

8.    (Currently Amended) The decoder ~~interface~~ of claim 7, wherein a first byte of the ~~raw byte~~ data causes a token header to be generated.

9.    (Currently Amended) The decoder ~~interface~~ of claim 8, wherein subsequent bytes of the ~~raw byte~~ data are appended to the token header to form tokens.

10.   (Currently Amended) A method ~~of operating an input circuit~~ to receive encoded data <u>embedded in a bit stream with different standards</u> ~~for decoding purposes~~ comprising:

        operating [[the]] <u>an</u> input ~~circuit~~ <u>device</u> in a first mode to receive ~~raw byte~~ data at a port ~~of the input circuit~~ from an external source;

        operating the input ~~circuit~~ <u>device</u> in a second mode to receive tokens at the port ~~of the input circuit~~ from said external source; and

providing a ~~plurality of stages, including an initial and an intermediate stage,~~ ~~said stages~~ stage having an associated parser, said tokens having information used by said parser to prepare said ~~stages~~ stage for processing underline different portions of said bit stream corresponding to said different standards.

11.   (Original) The method of claim 10, wherein the port is a coded data port.

12.   (Currently Amended) The method of claim 10, wherein the port is a microprocessor ~~interface~~.

13.   (Original) The method of claim 10, wherein a byte mode selects one of the first mode or the second mode.

14.   Canceled

15.   (Currently Amended) The method of claim 10, wherein operating the input ~~circuit~~ device in the first mode comprises:

   forming tokens from the received ~~raw byte~~ data.

16.   (Currently Amended) The method of claim 15, wherein forming tokens comprises:

   generating a token header in response to receiving a first byte of the ~~raw byte~~ data.

17.   (Currently Amended) The method of claim 16, further comprising:

appending subsequent bytes of the ~~raw byte~~ data to the generated token header.

18. (New) The method of claim 10, wherein the different standards include MPEG.

19. (New) The method of claim 10, wherein the different standards include JPEG.

20. (New) The method of claim 10, wherein the different standards include H.261.